· · · · · · · · · · · · · · · · · · ·	Application No.	Applicant(s)
Notice of Allowability	10/766,951 Examiner	TAKEOKA ET AL.
	Nadeem Iqbal	2114
The MAILING DATE of this communication appears on the cover sheet with the correspondence address All claims being allowable, PROSECUTION ON THE MERITS IS (OR REMAINS) CLOSED in this application. If not included herewith (or previously mailed), a Notice of Allowance (PTOL-85) or other appropriate communication will be mailed in due course. THIS NOTICE OF ALLOWABILITY IS NOT A GRANT OF PATENT RIGHTS. This application is subject to withdrawal from issue at the initiative of the Office or upon petition by the applicant. See 37 CFR 1.313 and MPEP 1308.		
1. X This communication is responsive to <u>Jan 30, 2004</u> .		
2. X The allowed claim(s) is/are <u>1-20</u> .		
 3. X Acknowledgment is made of a claim for foreign priority ur a) X All b) Some* c) None of the: 1. X Certified copies of the priority documents have 2. Certified copies of the priority documents have 	e been received.	
3. 🗌 Copies of the certified copies of the priority documents have been received in this national stage application from the		
International Bureau (PCT Rule 17.2(a)).		
* Certified copies not received:		
Applicant has THREE MONTHS FROM THE "MAILING DATE" of this communication to file a reply complying with the requirements noted below. Failure to timely comply will result in ABANDONMENT of this application. THIS THREE-MONTH PERIOD IS NOT EXTENDABLE.		
4. A SUBSTITUTE OATH OR DECLARATION must be submitted. Note the attached EXAMINER'S AMENDMENT or NOTICE OF INFORMAL PATENT APPLICATION (PTO-152) which gives reason(s) why the oath or declaration is deficient.		
5. CORRECTED DRAWINGS (as "replacement sheets") must be submitted.		
(a) 🗋 including changes required by the Notice of Draftsperson's Patent Drawing Review (PTO-948) attached		
1) 🗌 hereto or 2) 🔲 to Paper No./Mail Date		
(b) including changes required by the attached Examiner's Amendment / Comment or in the Office action of Paper No./Mail Date		
Identifying indicia such as the application number (see 37 CFR 1.84(c)) should be written on the drawings in the front (not the back) of each sheet. Replacement sheet(s) should be labeled as such in the header according to 37 CFR 1.121(d).		
 DEPOSIT OF and/or INFORMATION about the deposit of BIOLOGICAL MATERIAL must be submitted. Note the attached Examiner's comment regarding REQUIREMENT FOR THE DEPOSIT OF BIOLOGICAL MATERIAL. 		
Attachment(s) 1. X Notice of References Cited (PTO-892)	5 Thetics of Informal I	Detect Application (DTO 450)
2. D Notice of Draftperson's Patent Drawing Review (PTO-948)	6. 🗌 Interview Summary	Patent Application (PTO-152)
3. Information Disclosure Statements (PTO-1449 or PTO/SB/0	Paper No./Mail Da	ate
Paper No./Mail Date Jan 30, 04		
 Examiner's Comment Regarding Requirement for Deposit of Biological Material 	8. 🛛 Examiner's Statem	ent of Reasons for Allowance
	9. 🗌 Other	Nadific
		NADEEM IQBAL PRIMARY EXAMINER

The following is an Examiner's Statement of Reasons for Allowance:

The prior arts of record teach a method and apparatus for determining quality metrics associated with a test pattern used to test an integrated circuit. Metrics are formulated for the test of each individual delay fault, and these individual metrics are collected into respective IC-wide metrics. The key metrics, both at the individual fault level and the IC level referred as the Delay defect exposure and the delay sensitivity ratio. For both metrics, the delay time relative to the appropriate system clock associated with a longest sensitizable path through the IC that includes the fault site is determined as well as the delay time relative to the same system clock of the actual path activated by the test pattern. Rearick et al., U.S. Patent No. 6,708,139 is one such example of prior art. The prior arts of record, however, fails to teach, singly or in combination, evaluating a delay fault test quality by a method that comprises a deciding unit that decide whether or not a test can be performed for each delay fault selected by a delay fault selecting unit and calculating a delay fault coverage by using the number of the delay faults decided to be untestable by the deciding unit. The subject matter sought to be patented as recited in the claims in this application has practical applications in the field of delay fault testing of a semiconductor circuit.

Any comments considered necessary by applicant must be submitted no later than the payment of the Issue Fee and, to avoid processing delays, should preferably **accompany** the Issue Fee. Such submissions should be clearly labeled "Comments on Statement of Reasons for Allowance."

Application/Control Number: 10/766,951 Art Unit: 2114

Any inquiry concerning this communication or earlier communications from the examiner should be directed to Nadeem Iqbal whose telephone number is (703)-308-5228. The examiner can normally be reached on M-F (8:00-5:30) First Friday Off.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Robert W Beausoliel can be reached on (703)-305-9713. The fax phone number for the organization where this application or proceeding is assigned is (703)-872-9306.

Any inquiry of a general nature or relating to the status of this application or proceeding should be directed to the receptionist whose telephone number is (703)-305, 3900.

Nadeem Idbal Primary Examiner Art Unit 2114

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